Recent dawn of the Multi-Processor System-on-Chip (MPSoC) era in embedded design is triggered by the need to handle more complex applications, while reducing overall cost of embedded (handheld) devices. The most common approach to achieve these demanding goals is to use a platform-based design methodology. Still, running these applications on such a device with the most optimum operating efficiency (execution time, power,…) is an enormous challenge. With increasing time-to-market constraints, this challenge grows even more. MPSoC design methodology, developed at IMEC, is an attempt to assist users to reach optimum operating goals in a systematic, semi-automated manner.

References:

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